

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	("20030046492").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/04/28 09:05
S2	2909	(711/118,170,173).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/04/28 09:06
S3	336	((711/118,170,173).CCLS.) and @ad < "20010828") and (configur\$5 with memory with cache)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 09:11
S5	150	(configurable with memory with cache) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 09:41
S6	108	((configurable with memory with cache) and @ad < "20010828") and (chip IC (integrated adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 09:42
S4	239	configurable with memory with cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 09:42
S9	89	((configurable reconfigurable) near5 memory near5 cache) and @ad <"20010828") and (chip IC (integrated adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 09:48
S8	125	((configurable reconfigurable) near5 memory near5 cache) and @ad <"20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 09:48
S7	104	(configurable near5 memory near5 cache) and @ad <"20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 09:48

S11	0	("l18andfuse").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/04/28 15:53
S10	17	(US-6697916-\$ or US-6606686-\$ or US-6606684-\$ or US-6484237-\$ or US-6446181-\$ or US-6427190-\$ or US-6321318-\$ or US-5966734-\$ or US-6678790-\$ or US-6347346-\$ or US-6150839-\$).did. or (US-20020069331-\$ or US-20020078268-\$ or US-20020073282-\$ or US-20020069341-\$ or US-20020065867-\$ or US-20030046492-\$).did.	US-PGPUB; USPAT	OR	OFF	2004/04/28 15:53
S12	1	((US-6697916-\$ or US-6606686-\$ or US-6606684-\$ or US-6484237-\$ or US-6446181-\$ or US-6427190-\$ or US-6321318-\$ or US-5966734-\$ or US-6678790-\$ or US-6347346-\$ or US-6150839-\$).did. or (US-20020069331-\$ or US-20020078268-\$ or US-20020073282-\$ or US-20020069341-\$ or US-20020065867-\$ or US-20030046492-\$).did.) and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 15:54
S13	3	((US-6697916-\$ or US-6606686-\$ or US-6606684-\$ or US-6484237-\$ or US-6446181-\$ or US-6427190-\$ or US-6321318-\$ or US-5966734-\$ or US-6678790-\$ or US-6347346-\$ or US-6150839-\$).did. or (US-20020069331-\$ or US-20020078268-\$ or US-20020073282-\$ or US-20020069341-\$ or US-20020065867-\$ or US-20030046492-\$).did.) and burn	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 15:58
S15	350	((memory with configur\$5) with fuse) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 15:59
S14	608	((memory with configur\$5) same fuse) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/28 15:59

S18	0	("9591539").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/04/29 09:19
S17	1	("0591539").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/04/29 09:19
S21	33	"09/591537"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 09:20
S20	0	"09591537"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 09:20
S19	0	"09591539"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 09:20
S16	78	((memory with configur\$5) with fuse) and @ad < "20010828") and processor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 13:52
S24	34	((chip adj stack) and (integrated adj circuit) and @ad < "20010828") and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 13:55
S22	9656	((chip adj stack) (flip adj chip)) and (integrated adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 14:05
S25	6794	(flip adj chip) and (integrated adj circuit) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 14:06
S27	1464760	(( (flip adj chip) and (integrated adj circuit) and @ad < "20010828") and fuse) memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 14:08

S26	426	( (flip adj chip) and (integrated adj circuit) and @ad < "20010828") and fuse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 14:08
S29	0	(( (flip adj chip) and (integrated adj circuit) and @ad < "20010828") and fuse) and memory) and (chip adj stack)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 14:11
S28	199	(( (flip adj chip) and (integrated adj circuit) and @ad < "20010828") and fuse) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 14:11
S23	389	(chip adj stack) and (integrated adj circuit) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 14:12
S30	21	(US-6697916-\$ or US-6606686-\$ or US-6606684-\$ or US-6484237-\$ or US-6446181-\$ or US-6427190-\$ or US-6321318-\$ or US-5966734-\$ or US-6678790-\$ or US-6347346-\$ or US-6150839-\$ or US-6355968-\$ or US-6297666-\$).did. or (US-20020069331-\$ or US-20020078268-\$ or US-20020073282-\$ or US-20020069341-\$ or US-20020065867-\$ or US-20030046492-\$ or US-20010049174-\$ or US-20020074668-\$).did.	US-PGPUB; USPAT	OR	OFF	2004/04/29 15:12
S31	1	((US-6697916-\$ or US-6606686-\$ or US-6606684-\$ or US-6484237-\$ or US-6446181-\$ or US-6427190-\$ or US-6321318-\$ or US-5966734-\$ or US-6678790-\$ or US-6347346-\$ or US-6150839-\$ or US-6355968-\$ or US-6297666-\$).did. or (US-20020069331-\$ or US-20020078268-\$ or US-20020073282-\$ or US-20020069341-\$ or US-20020065867-\$ or US-20030046492-\$ or US-20010049174-\$ or US-20020074668-\$).did.) and ((macro adj cell) macrocell)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 15:14

S34	0	((configurable with memory) same (ram and cache)) and (macro adj cell) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 15:16
S33	0	(configurable with memory) same (ram) same (macro adj cell) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 15:16
S32	0	(configurable with memory) same (ram and cache) same (macro adj cell) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 15:16
S35	9	((configurable with memory) same (ram)) and (macro adj cell) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 15:25
S36	246	(memory) same (ram) and (macro adj cell) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/29 15:26
S37	2	("20030046492").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/28 16:25
S38	2562	((711/118,170,173).CCLS.) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/28 17:16
S40	90	((configurable near5 memory near5 cache) and @ad <"20010828") and (chip IC (integrated adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/28 17:17
S39	37	(((711/118,170,173).CCLS.) and @ad < "20010828") and (configur\$5 with memory with cache)) and (configurable with memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/28 17:17
S41	43	((configur\$5 with memory) same (ram)) and (macro adj cell) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/28 17:18

S42	80	((chip adj stack) and (integrated adj circuit) and @ad < "20010828") and (flip adj chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/28 17:19
S43	0	("US20030046492A1").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:54
S44	1	US20030046492A1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/14 09:55
S45	1	09/940709	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 15:17
S46	361	((711/118,170,173).CCLS.) and @ad < "20010828") and (configur\$5 with memory with cache)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 19:54
S48	166	(configurable with memory with cache) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 19:55
S47	310	configurable with memory with cache	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 19:55
S49	140	((configurable reconfigurable) near5:memory near5:cache) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 19:57
S51	45	09/591537	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 21:11
S50	79	S49 and tag	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/11 21:11

L1	1	09/940709	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 14:04
L4	364	((711/118,170,173).CCLS.) and @ad <"20010828") and (configur\$5 with memory with cache)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 16:04
L3	4134	(711/118,170,173).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/27 16:04
L5	141	((configurable reconfigurable) near5 memory near5 cache) and @ad <"20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 16:05